

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10001477
	Filing Date		2001-11-01
	First Named Inventor	Steve Roe	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01203M	

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/JP/	1	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
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/JP/	1	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>
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/JP/	4	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>

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/JP/	1	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	ABS only

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/JP/	10	USPTO Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer," Nemecek et al., filed on 11/01/2001; 47 pages	<input type="checkbox"/>
/JP/	11	USPTO Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System," Craig Nemecek, filed on 11/01/2001; 43 pages	<input type="checkbox"/>

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/JP/	12	USPTO Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer," Nemecek et al., filed on 11/14/2001; 46 pages	<input type="checkbox"/>
/JP/	13	USPTO Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control," Nemecek et al., filed on 11/14/2001; 47 pages	<input type="checkbox"/>
/JP/	14	USPTO Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller," Sullam et al., filed on 10/24/2001; 34 pages	<input type="checkbox"/>
/JP/	15	USPTO Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller," Kutz et al., filed on 06/22/2001; 44 pages	<input type="checkbox"/>
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/JP/	17	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
/JP/	18	USPTO Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Warren Snyder, filed on 10/15/2002; 36 pages	<input type="checkbox"/>
/JP/	19	USPTO Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control," Shutt et al., filed on 10/11/2001; 35 pages	<input type="checkbox"/>
/JP/	20	USPTO Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm," Warren Snyder, filed on 10/09/2001; 26 pages	<input type="checkbox"/>
/JP/	21	The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; Snyder et al., filed on 10/26/2000; 277 pages	<input type="checkbox"/>
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/JP/	23	USPTO Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface," Warren Snyder, filed on 10/05/2001; 32 pages	<input type="checkbox"/>
/JP/	24	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
/JP/	25	USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Sullam et al., filed on 10/25/2001; 49 pages	<input type="checkbox"/>
/JP/	26	USPTO Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode," Warren Snyder, filed on 10/05/2001; 31 pages	<input type="checkbox"/>

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/JP/	1	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; 4 pages	<input type="checkbox"/>
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/JP/	1	6107769	B1	2000-08-22	Saylor et al.	Entire Document
/JP/	2	7073158	B1	2006-07-04	McCubrey	Entire Document
/JP/	3	6642857	B1	2003-11-04	Schediwy et al.	Entire Document
/JP/	4	6263339	B1	2001-07-17	Hirsch	Entire Document
/JP/	5	6282547	B1	2001-08-28	Hirsch	Entire Document
/JP/	6	6678877	B1	2004-01-13	Perry, et al.	Entire Document
/JP/	7	5889236	B1	1999-03-30	Gillespie et al.	Entire Document
/JP/	8	6661288	B1	2003-12-09	Morgan et al.	Entire Document

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/JP/	9	6061511	B1	2000-05-09	Marantz et al.	Entire Document
/JP/	10	6211708	B1	2001-04-03	Klemmer	Entire Document
/JP/	11	6204687	B1	2001-03-20	Schutiz et al.	Entire Document
/JP/	12	6850554	B1	2005-02-01	Sha	Entire Document
/JP/	13	7138841	B1	2006-11-21	Li	Entire Document
/JP/	14	5594388	B1	1997-01-14	O'Shaughnessy et al.	Entire Document
/JP/	15	5952888	B1	1999-09-14	Scott	Entire Document
/JP/	16	6680632	B1	2004-01-20	Meyers et al.	Entire Document
/JP/	17	6683930	B1	2004-01-27	Dalmia	Entire Document
/JP/	18	7138868	B1	2006-11-21	Sanchez et al.	Entire Document
/JP/	19	6617888	B1	2003-09-09	Volk, Andrew M.	Entire Document

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/JP/	20	7030688	B1	2006-04-18	Dosho et al.	Entire Document
/JP/	21	5861583	B1	1999-01-19	Schediwy et al.	Entire Document
/JP/	22	6788116	B1	2004-09-07	Cook et al.	Entire Document
/JP/	23	6289489	B1	2001-09-11	Bold et al.	Entire Document
/JP/	24	5276890	B1	1994-01-04	Arai, Makoto	Entire Document
/JP/	25	6691301	B1	2004-02-10	Bowen	Entire Document
/JP/	26	5966532	B1	1999-10-12	McDonald et al.	Entire Document
/JP/	27	6311149	B1	2001-10-30	Ryan et al.	Entire Document
/JP/	28	6388109	B1	2002-05-14	Schwarz et al.	Entire Document
/JP/	29	5682032	B1	1997-10-28	Philipp	Entire Document
/JP/	30	6750889	B1	2004-06-15	Livingston et al.	Entire Document

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/JP/	31	5689195	B1	1997-11-18	Cliff et al.	Entire Document
/JP/	32	6799198	B1	2004-09-28	Huboi et al.	Entire Document
	33	7152037	B1	2006-12-19	Adreede et al.	Entire Document
/JP/	34	3600690	B1	1971-08-17	White, Colin Graham	Entire Document
/JP/	35	4380083	B1	1983-04-12	Andersson et al.	Entire Document
/JP/	36	5917356	B1	1999-06-29	Casal et al.	Entire Document
/JP/	37	7295049	B1	2007-11-13	Moyal et al.	Entire Document
/JP/	38	5491458	B1	1996-02-13	McCune	Entire Document
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/JP/	40	5886582	B1	1999-03-23	Stansell	Entire Document
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/JP/	42	5331315	B1	1994-07-19	Crosetto	Entire Document
/JP/	43	6888453	B1	2005-05-03	Lutz et al.	Entire Document
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/JP/	1	20030126947	A1	2003-07-10	Margaria	Entire Document

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/JP/	2	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages	<input type="checkbox"/>
/JP/	3	USPTO Application Number 09/975,105 (CD00184): "Host to FPGA Interface in an In-Circuit Emulation System," Craig Nemecek, filed on 10/10/2001; 44 pages	<input type="checkbox"/>

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/JP/	2	7023215	B1	2006-04-04	Steenwyk	Entire Document
/JP/	3	7026861	B1	2006-04-11	Steenwyk	Entire Document
/JP/	4	7466307	B1	2008-12-16	Trent Jr. et al.	Entire Document
/JP/	5	5781747	B1	1998-07-14	Smith et al.	Entire Document
/JP/	6	5805792	B1	1998-09-08	Swoboda et al.	Entire Document
/JP/	7	7406674	B1	2001-10-24	Ogami et al.	Entire Document
/JP/	8	7103108	B1	2006-09-05	Beard	Entire Document

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/JP/	11	5889936	B1	1999-03-30	Chan	Entire Document
/JP/	12	5745011	B1	1998-04-28	Scott	Entire Document
/JP/	13	6784821	B1	2004-08-31	Lee, Soo-Hyoung	Entire Document
/JP/	14	5614861	B1	1997-03-25	Harada, Hiroyuki	Entire Document
/JP/	15	7283151	B1	2007-10-16	Nihei et al.	Entire Document
/JP/	16	6718533	B1	2004-04-06	Schneider et al.	Entire Document
/JP/	17	6611276	B1	2003-08-26	Muratori et al.	Entire Document
/JP/	18	6823497	B1	2004-11-23	Schubert et al.	Entire Document
/JP/	19	6865429	B1	2005-03-08	Schneider et al.	Entire Document

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/JP/	21	6601236	B1	2003-07-29	Curtis	Entire Document
/JP/	22	5479643	B1	1995-12-26	Bhaskar et al.	Entire Document
/JP/	23	6211741	B1	2001-04-03	Dalmia	Entire Document
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/JP/	26	6667642	B1	2003-12-23	Moyal	Entire Document
/JP/	27	6369660	B1	2002-04-09	Wei	Entire Document
/JP/	28	6377646	B1	2002-04-23	Sha	Entire Document
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/JP/	31	6075941	B1	2000-06-13	Itoh et al.	Entire Document
/JP/	32	6289300	B1	2001-09-11	Brannick et al.	Entire Document
/JP/	33	6298320	B1	2001-10-02	Buckmaster et al.	Entire Document
/JP/	34	6032268	B1	2000-02-29	Swoboda et al.	Entire Document
/JP/	35	6347395	B1	2002-02-12	Payne et al.	Entire Document
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	1	Adnan et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE, 6 pages DATE	<input type="checkbox"/>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10001477
	Filing Date		2001-11-01
	First Named Inventor	Steve Roe	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01203M	

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/JP/	1	6554469	B1	2003-04-29	Thomson et al.	Entire Document
/JP/	2	6149299	B1	2000-11-21	Asian et al.	Entire Document
/JP/	3	6008685	B1	1999-12-28	Kunst, David J.	Entire Document
/JP/	4	6157024	B1	2000-12-05	Chapdelaine et al.	Entire Document
/JP/	5	7176701	B1	2007-02-13	Wachi et al.	Entire Document
/JP/	6	7281846	B1	2007-10-16	McLeod, Scott C.	Entire Document
	7	7176701	B1	2007-02-13	Wachi et al.	Entire Document
/JP/	8	7227389	B1	2007-06-05	Gong et al.	Entire Document

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/JP/	9	7091713	B1	2006-08-15	Erdelyi et al.	Entire Document
/JP/	10	6121791	B1	2000-09-19	Abbott	Entire Document
/JP/	11	6449628	B1	2002-09-10	Wasson	Entire Document
/JP/	12	5761128	B1	1998-06-02	Watanabe, Kazuo	Entire Document
/JP/	13	6208572	B1	2001-03-27	Adams et al.	Entire Document
/JP/	14	6407953	B1	2002-06-18	Cleeves, James M.	Entire Document
/JP/	15	6650581	B1	2003-11-18	Hong et al.	Entire Document
/JP/	16	6731552	B1	2004-05-04	Perner, Martin	Entire Document
/JP/	17	6862240	B1	2005-03-01	Burgan, John M.	Entire Document
/JP/	18	7301835	B1	2007-11-27	Joshi et al.	Entire Document
/JP/	19	4972372	B1	1990-11-20	Ueno, Kouji	Entire Document

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/JP/	20	7376001	B1	2008-05-20	Joshi et al.	Entire Document
/JP/	21	5463591	B1	1995-10-31	Aimoto et al.	Entire Document
/JP/	22	5963503	B1	1999-10-05	Lee, Young-Dae	Entire Document
/JP/	23	5896330	B1	1999-04-20	Gibson, Garnet Frederick Randall	Entire Document
/JP/	24	6577258	B1	2003-06-10	Ruha et al.	Entire Document
/JP/	25	5198817	B1	1993-03-30	Walden et al.	Entire Document
/JP/	26	7129873	B1	2006-10-30	Kawamura, Akinobu	Entire Document
/JP/	27	6183131	B1	2001-02-06	Holloway et al.	Entire Document
/JP/	28	7009444	B1	2006-03-07	Scott, Greg	Entire Document
/JP/	29	6853598	B1	2005-02-08	Chevallier, Christopher J.	Entire Document
/JP/	30	6215352	B1	2001-04-10	Sudo, Naoaki	Entire Document

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/JP/	31	5530673	B1	1996-06-25	Tobita et al.	Entire Document
/JP/	32	7358714	B1	2008-04-15	Watanabe et al.	Entire Document
/JP/	33	7193901	B1	2007-03-20	Ruby et al.	Entire Document
/JP/	34	7554847	B1	2009-06-30	Lee, Jong-Soo	Entire Document
/JP/	35	6253754	B1	2001-07-03	Reichpeter	Entire Document
/JP/	36	6101617	B1	2000-08-08	Burckhardt et al.	Entire Document
/JP/	37	6894928	B1	2005-05-17	Owen	Entire Document
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U.S. PATENT APPLICATION PUBLICATIONS

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/JP/	1	20080095213	A1	2008-04-24	Lin et al.	Entire Document

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/JP/	2	20080259998	A1	2008-10-23	Venkataraman et al.	Entire Document
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FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
/JP/	1	DE 19710829	DE	A1	1998-09-24	Goloub, Boris	Entire Document	NO <input type="checkbox"/>

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NON-PATENT LITERATURE DOCUMENTS

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/JP/	1	USPTO Non-Final Rejection for Application Number 11/865,672 (CD2007098) dated 07/17/2009; 6 pages	<input type="checkbox"/>
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/JP/	6	USPTO Non-Final Rejection for Application Number 12/104,672 (CD2007138) dated 08/26/2009; 11 pages	<input type="checkbox"/>
/JP/	7	USPTO Non-Final Rejection for Application Number 12/060,128 (CD2007143) dated 04/29/2009; 11 pages	<input type="checkbox"/>
/JP/	8	International Search Report of the International Searching Authority for International Application Number PCT/US08/60695 (CD2007132) dated 07/22/2009; 3 pages	<input type="checkbox"/>
/JP/	9	Written Opinion of the International Searching Authority for International Application Number PCT/US08/60695 (CD2007132) dated 07/22/2009; 6 pages	<input type="checkbox"/>
/JP/	10	Azim et al., "A Custom DSP Chip to Implement a Robot Motion Controller Proceedings of the IEEE Custom Integrated Circuits Conference," May 1988, pp. 8.7.1-8.7.5; 6 pages	<input type="checkbox"/>
/JP/	11	Catthoor et al., "Architectural Strategies for an Application-Specific Synchronous Multiprocessor Environment," IEEE transactions on Acoustics, Speech, and Signal Processing; Vol. 36, No. 2, Feb. 1988, pp. 265-284; 20 pages	<input type="checkbox"/>
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/JP/	13	International Search Report of the International Searching Authority for International Application Number PCT/US08/60698 (CD2007116) dated 09/05/2008; 2 pages	<input type="checkbox"/>
/JP/	14	Shahbahrami et al., "Matrix Register File and Extended Subwords: Two Techniques for Embedded Media Processors," ACM, May 2005; 9 pages	<input type="checkbox"/>
/JP/	15	Jung et al., "A Register File with Transposed Access Mode," 2000, IEEE; 2 pages	<input type="checkbox"/>
/JP/	16	International Search Report of the International Searching Authority for International Application Number PCT/US08/60681 (CD07139) dated 09/12/2008; 2 pages	<input type="checkbox"/>

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/JP/	17	Written Opinion of the International Searching Authority for International Application Number PCT/US08/60681 (CD07139) dated 09/12/2008; 4 pages	<input type="checkbox"/>
/JP/	18	USPTO Notice of Allowance for Application Number 11/965,677 (CD2007132) dated 09/10/2009; 6 pages	<input type="checkbox"/>
/JP/	19	USPTO Non-Final Rejection for Application Number 11/965,677 (CD2007132) dated 03/10/2009; 10 pages	<input type="checkbox"/>
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Examiner Signature	/Jason Proctor/	Date Considered	11/30/2009
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☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Lucinda G. Price/	Date (YYYY-MM-DD)	2009-11-02
Name/Print	Lucinda G. Price	Registration Number	42270

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